

Passive module - FLKM 16/AO/SI/DV - 2304445

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Fuse module (for analog output cards of the Delta V control system)

Product Features

- ✓ These system-specific interface modules for DeltaV modules are used in combination with the relevant system cables
- ✓ 1:1 connection
- ✓ 5 x 20, 50 mA T, IEC 60127-2/3 fuse per channel



Key commercial data

Packing unit	1 pc
Weight per Piece (excluding packing)	200.5 GRM
Custom tariff number	85369010
Country of origin	Germany

Technical data

Dimensions

Width	90 mm
Height	90 mm
Depth	68 mm

Ambient conditions

Ambient temperature (operation)	-20 °C ... 50 °C
Ambient temperature (storage/transport)	-20 °C ... 70 °C

General

Nominal voltage U_N	< 50 V AC
	60 V DC
Max. current carrying capacity per branch	50 mA (In delivered state, with one 50 mA fuse, max. 1 A permitted)
Number of positions	16

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Technical data

General

Status display	No
Test voltage	500 V (50 Hz, 1 min.)
Test voltage contact/contact	500 V AC
Mounting position	any
Assembly instructions	In rows with zero spacing
Standards/regulations	IEC 60664
	DIN EN 50178
	IEC 62103
Pollution degree	2
Surge voltage category	III
Input operating voltage	24 V DC \pm 10 %
Max. current carrying capacity per branch	50 mA (With 50 mA fuse on delivery)
Rated surge voltage	0.8 kV

Connection data for connection 1

Connection name	Field level
Connection in acc. with standard	IEC / EN
Connection method	Screw connection
Conductor cross section solid min.	0.2 mm ²
Conductor cross section solid max.	4 mm ²
Conductor cross section stranded min.	0.2 mm ²
Conductor cross section stranded max.	2.5 mm ²
Conductor cross section AWG/kcmil min.	24
Conductor cross section AWG/kcmil max	12
Stripping length	8 mm
Screw thread	M3

Connection data for connection 2

Connection name	Control system level
Number of connections	1
Connection method	IDC/FLK pin strip (2.54 mm)
Number of positions	16

Supported controller

Controller	Emerson DeltaV
- suitable I/O card	VE4005S2B3

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Classifications

eCl@ss

eCl@ss 4.0	27250313
eCl@ss 4.1	27250313
eCl@ss 5.0	27250313
eCl@ss 5.1	27250313
eCl@ss 6.0	27371601
eCl@ss 7.0	27371601
eCl@ss 8.0	27371601

ETIM

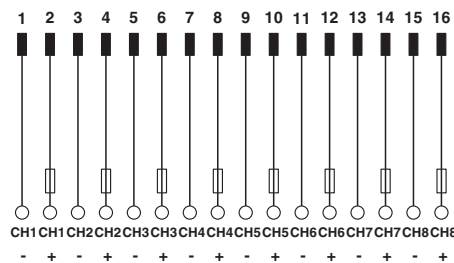
ETIM 2.0	EC001437
ETIM 3.0	EC001437
ETIM 4.0	EC001437
ETIM 5.0	EC001437

UNSPSC

UNSPSC 6.01	30211824
UNSPSC 7.0901	39121421
UNSPSC 11	39121421
UNSPSC 12.01	39121421
UNSPSC 13.2	39121421

Drawings

Circuit diagram



FLKM 16/AO/SI/DV connection scheme